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Substitute for form 1448A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/565,674
		Filing Date	January 24, 2006
		First Named Inventor	Thomas Bierhoff
		Art Unit	Not yet assigned 2874
		Examiner Name	Not yet assigned KAJLI PRINCE
Sheet 1	of 2	Attorney Docket Number	449122086300

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
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		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
KP	1	DE 10051405 C2= WO 02 33455 A2	04-25-2002	Siemens AG		

Examiner Signature	/Kajli Prince/	Date Considered	02/12/2007
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 2 of 2

Complete if Known

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KP	2	E. GRIESE ET AL.: "Time domain simulation of optical multimode chip-to-chip interconnects" PROCEEDINGS OF SPIE, Vol. 4455, December 2001, pages 131-142, XP002304825, paragraph 0002, pictures 5-17	
KP	3	E. CZECH: "Spatial distribution of luminance of light emitted by optical fibers", PROCEEDINGS OF SPIE, Vol. 5064, April 2003, pages 222-229, CP002304826, paragraph 0001-0005, pictures 1-5	
KP	4	BIERHOFF T. ET AL: "Ray Tracing Technique and its verification for the analysis of highly multimode optical waveguides with rough surfaces"; IEEE Transactions on Magnetics, IEEE Inc. New York, US, Vol. 37, No. 5, Part 1 9 (2001) pages 3307-3310, XP 001089831, ISSN: 0018-9464, completed document	
KP	5	BIERHOFF T. ET AL: "An approach to model wave propagation in highly multimode optical waveguides with rough surfaces" International symposium on Theoretical electrical engineering, XX, XX, September 6, 1999, pages 515-520, XP008005522, completed document	

Examiner
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/Kajli Prince/

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